

1/7

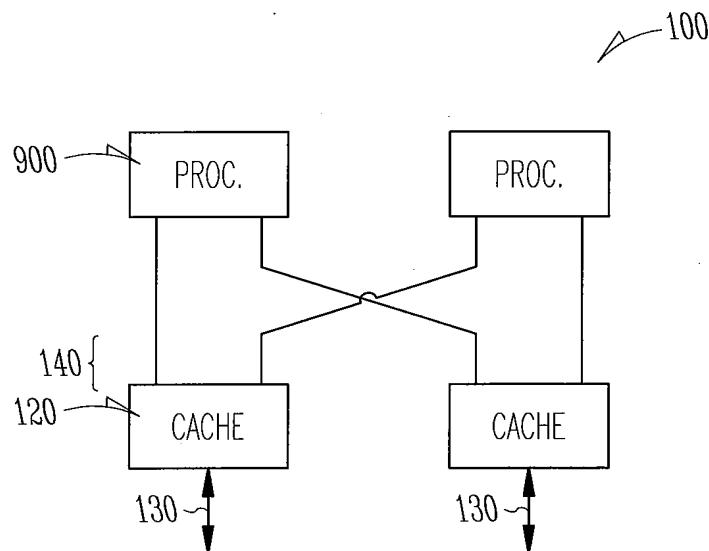


Fig. 1

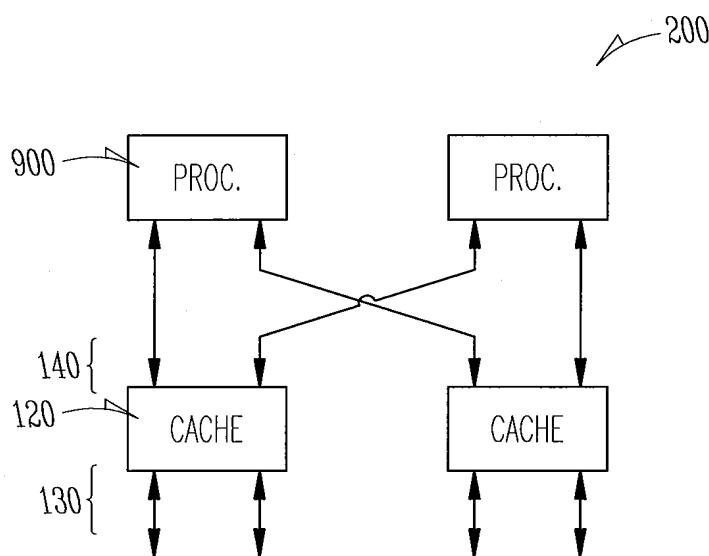


Fig. 2

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300

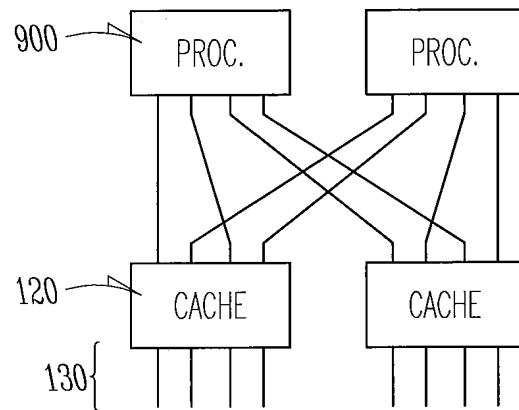


Fig. 3

400

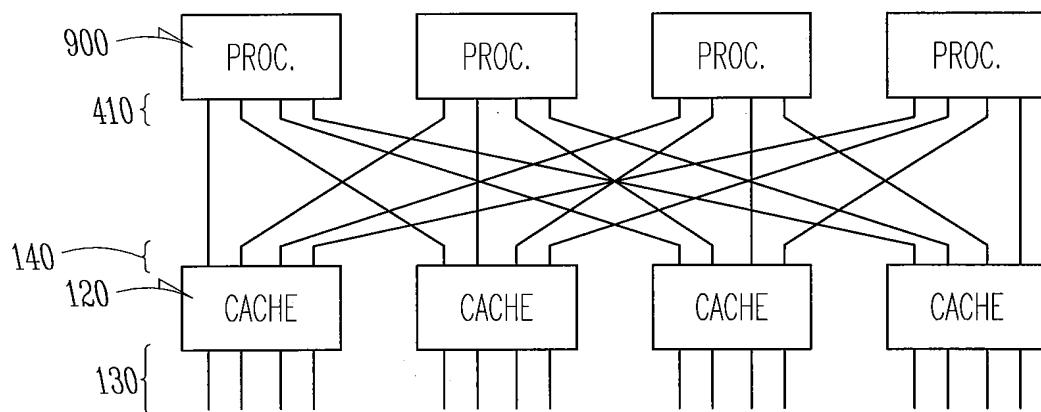


Fig. 4

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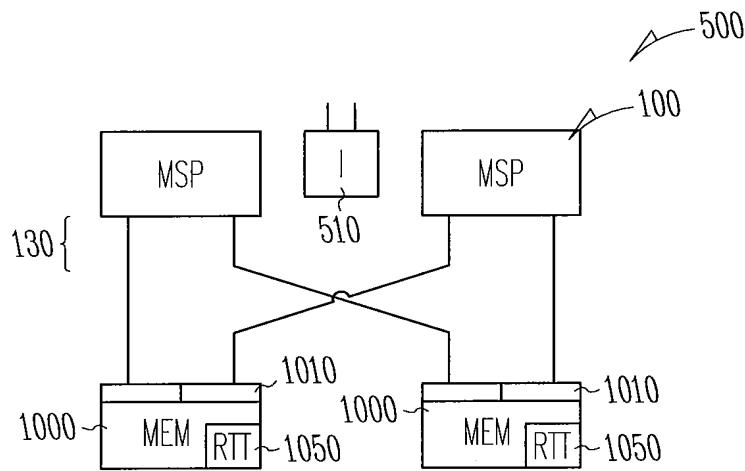


Fig. 5

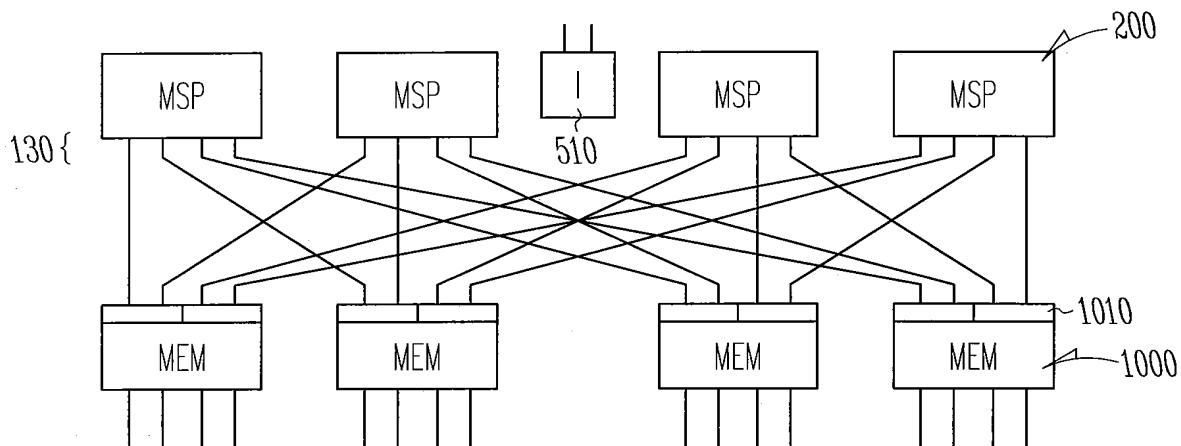


Fig. 6

TITLE: LATENCY TOLERANT DISTRIBUTED SHARED MEMORY MULTIPROCESSOR COMPUTER

INVENTOR'S NAME: STEVEN L. SCOTT

SERIAL NO.: 10/643,585 DOCKET NO.: 1376.700US1

REPLACEMENT SHEET

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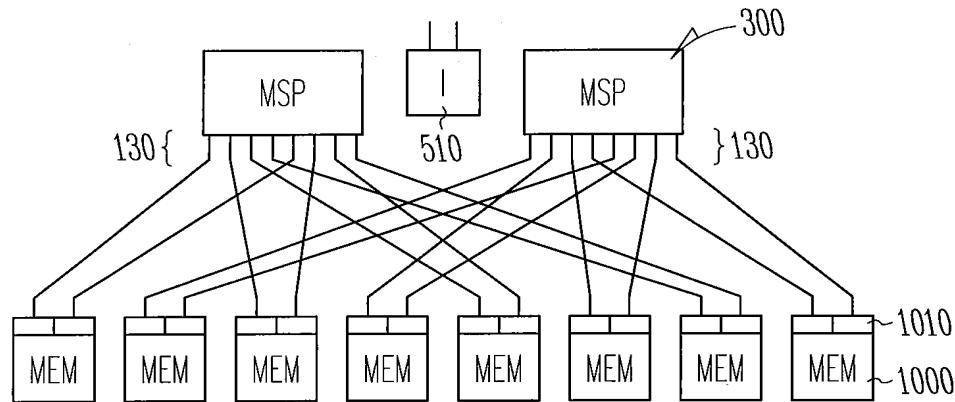


Fig. 7

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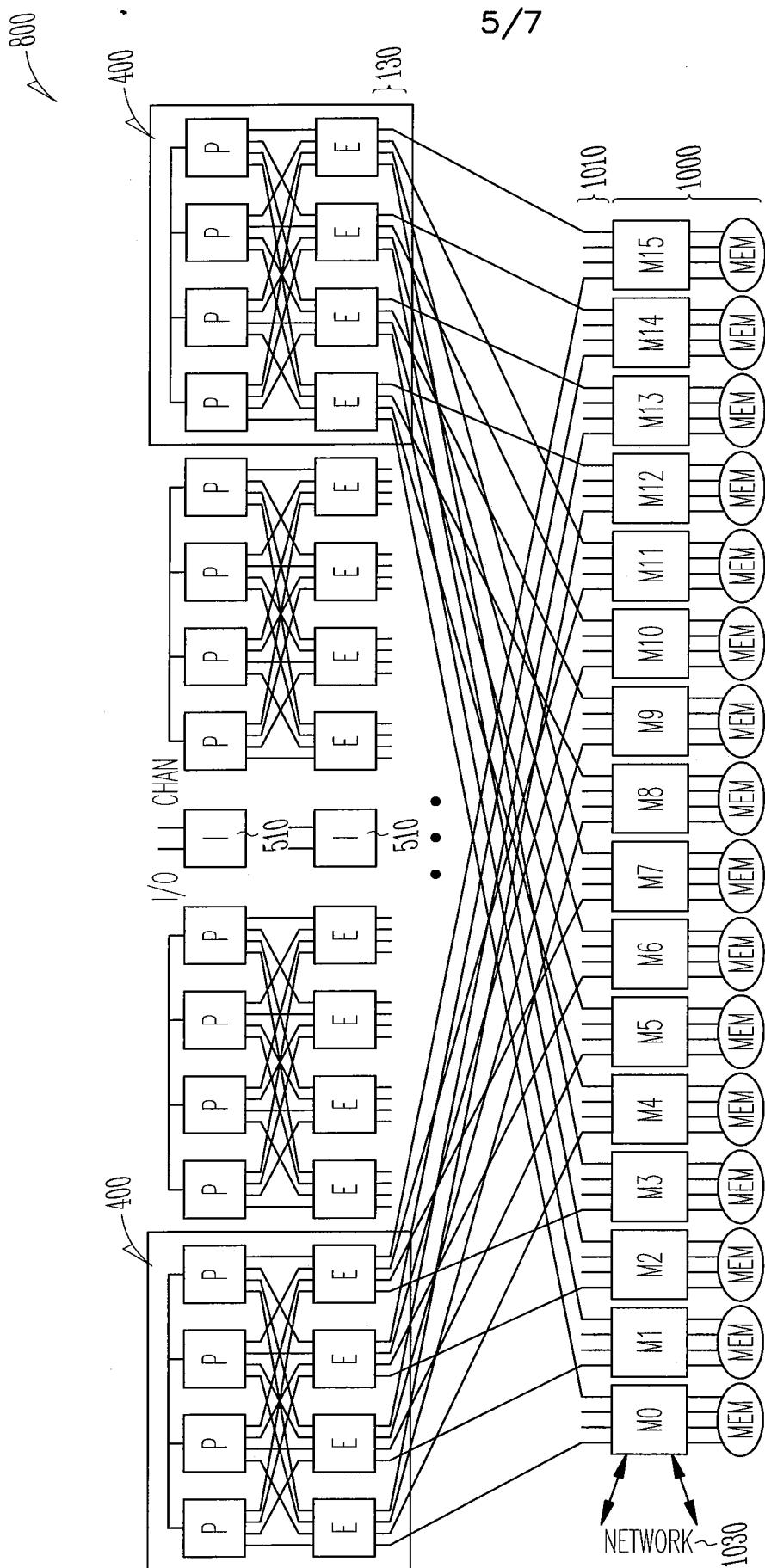
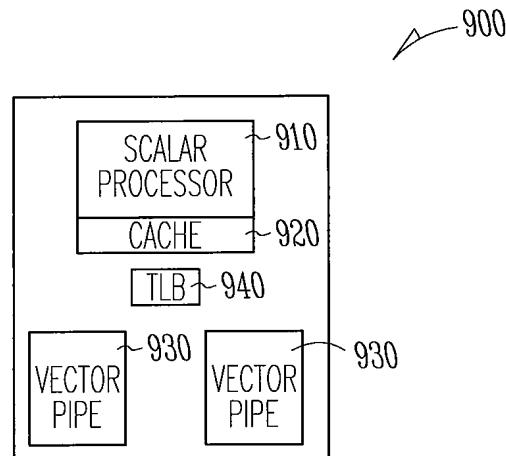
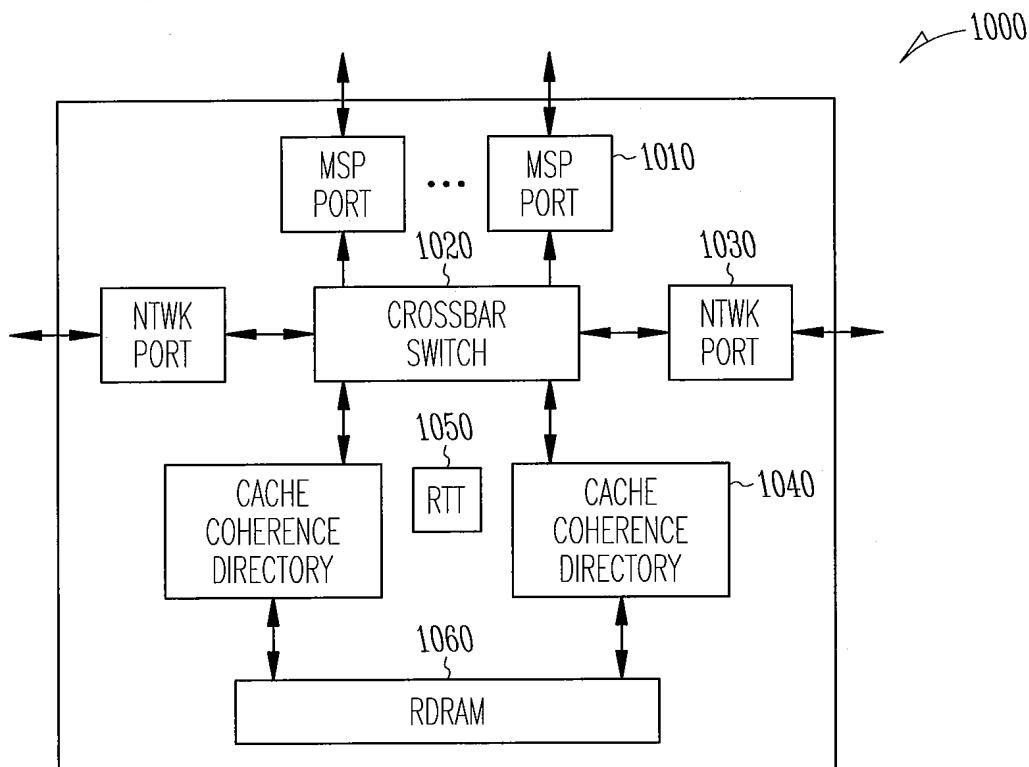


Fig. 8

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*Fig. 9**Fig. 10*

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1100

VIRTUAL ADDRESS FORMAT

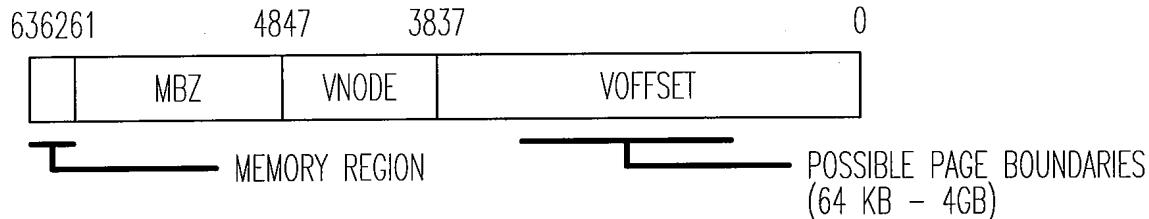


Fig. 11

1200

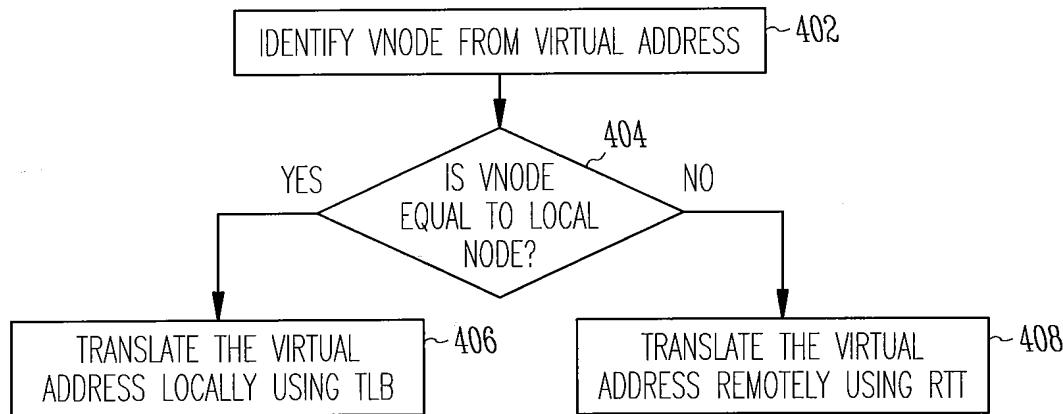


Fig. 12